TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WH240FU,TC7WH240FK

Dual Bus Buffer Inverted, 3-State Outputs

The TC7WH240 is an advanced high speed CMOS DUAL BUS BUFFERS fabricated with silicon gate CMOS technology.

They schieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. The 7WH240 is an inverting 3-state buffer having two active-low output enables.

This device is designed to be used with 3-state memory address drivers, etc.

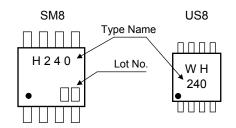
An input protection circuit ensures that 0 to 7 V can be applied to the input pins without regard to the supply voltage.

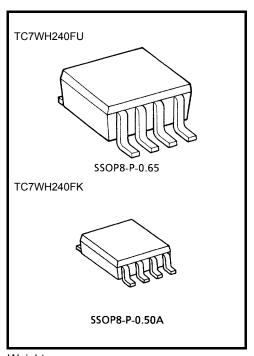
This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: t_{pd} = 3.6 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: $I_{CC} = 2 \mu A (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- 5.5V Tolerant inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2~5.5 V
- Low Noise : V_{OLP} = 0.8 V (max.)

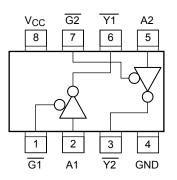
Marking





Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

Pin Assignment (top view)



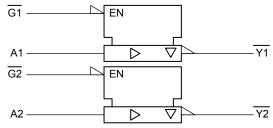
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	IIК	-20	mA
Output diode current	IOK	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	D-	300 (SM8)	m)//
	PD	200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Logic Diagram



Truth Table

INP	UTS	OUTPUTS				
ю	А	Y				
L	L	Н				
L	Н	L				
Н	Х	Z				
V . Den't Care						

X : Don't Care Z : High Impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 \pm 0.3 V)	ns/V	
	avav	0~20 (V _{CC} = 5 \pm 0.5 V)		

Electrical Characteristics

DC Characteristics

					Ta = 25°C			Ta = -40~85°C		
Characteristics Symbol Test Condition		ondition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
				2.0	1.50	_		1.50		
High-level input voltage	VIH	—		3.0~ 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	V
		_		2.0			0.50		0.50	V
Low-level input voltage	VIL			3.0~ 5.5	_		V _{CC} × 0.3	_	$V_{CC} \times 0.3$	
	Vон	VIN = VIH or VIL	I _{OH} = -50 μA	2.0	1.9	2.0		1.9	_	V
				3.0	2.9	3.0		2.9	_	
High-level output voltage				4.5	4.4	4.5		4.4	_	
			I _{OH} = -4 mA	3.0	2.58			2.48		
			I _{OH} = -8 mA	4.5	3.94	_		3.80	_	
	Vol	VIN = VIH or VIL	I _{OL} = 50 μA	2.0		0.0	0.1		0.1	-
				3.0		0.0	0.1		0.1	
Low-level output voltage				4.5		0.0	0.1	_	0.1	V
			$I_{OL} = 4 \text{ mA}$	3.0		_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5		_	0.36	_	0.44	
3-State Output Off-State Current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		5.5	_	—	0.25	—	2.50	μΑ
Input leakage current	I _{IN}	$V_{IN} = 5.5 V \text{ or GND}$		0~ 5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND		5.5			2.0	_	20.0	μA

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol Test Condition				Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Symbol R		V _{CC} (V)	C _L (pF)	Min.	Тур.	Max.	Min.	Max.	Unit
Propagation Delay Time	t _{pLH}	3.3 ± 0.3 5.0 ± 0.5	3.3 ± 0.3	15	_	5.3	7.5	1.0	9.0	
				50	_	7.8	11.0	1.0	12.5	
	t _{pHL}		15	_	3.6	5.5	1.0	6.5	ns	
			5.0 ± 0.5	50	_	5.1	7.5	1.0	8.5	
		$R_L = 1k\Omega$	$\textbf{3.3}\pm\textbf{0.3}$	15	_	6.6	10.6	1.0	12.5	ns
3-State Output Enable Time	t _{pZL} R _L =			50	_	9.1	14.1	1.0	16.0	
			5.0 ± 0.5	15	_	4.7	7.3	1.0	8.5	
				50	_	6.2	9.3	1.0	10.5	
3-State Output Disable Time	t _{pLZ}	$R_L = 1k\Omega$	3.3 ± 0.3	50	_	10.3	14.0	1.0	16.0	ns
	t _{pHZ}		5.0 ± 0.5	50	_	6.7	9.2	1.0	10.5	115
Output to Output	tos _{LH}		3.3 ± 0.3	50	_	_	1.5	_	1.5	ns
Skew	tos _{HL}	(Note 1)	5.0 ± 0.5	50	_	_	1.0	_	1.0	115
Input Capacitance	C _{IN}				_	4	10	_	10	pF
Output Capacitance	C _{I/O}				_	6	_	_	_	pF
Power Dissipation Capacitance (Note 2)	C _{PD}				_	17		_	_	pF

Note 1: Parameter guaranteed by design.

tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|

Note 2:C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

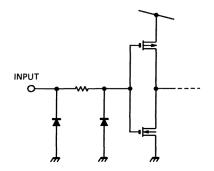
Average operating current can be obtained by the equation :

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Noise Characteristics (Ta = 25° C, input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V_{OL}	V _{OLP}	C _L = 50 pF	5.0	0.3	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0		3.5	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0		1.5	V

Input Equivalent Circuit

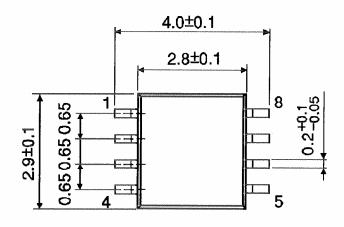


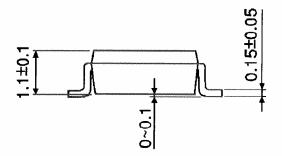
TOSHIBA

Package Dimensions

SSOP8-P-0.65

Unit : mm



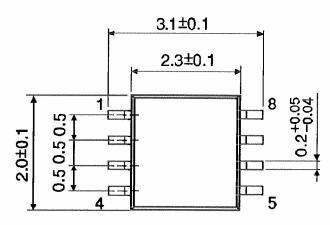


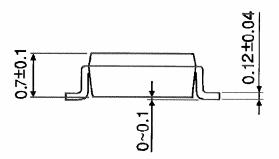
Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.).These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in his document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS compatibility. Please use these products in this document in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses occurring as a result of noncompliance with applicable laws and regulations.